Programme Registration Form

Name: _____
Designation:_____
Name of the Institution/ Industry:
_____
E-mail: _____
Mobile: _____

Signature of participant

Sponsoring Authority (Signature and Stamp)

Scan above image & send on email for confirmation.

Date: 20th& 21st Aug 2018
Time: 10.30 AM onwards

Venue: **Department of Mechanical & Automation Engineering (MAE)**
G.B. Pant Government Engineering College, Okhla Industrial Estate Phase III, New Delhi, 110020

Please Note:

1. **No registration fees.**
2. **Participation certificate will be provided.**
3. **In time registration is kindly requested**

Contact for registration and other details

Programme Coordinators

Dr. Anurag Dixit **+91- 9310687812**
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Industry Members

Miss. Sikkimee Kumbhar **+91- 8552970613**
Mr. Aniket Bhelsaikar **+91- 8796140143**

Registration by email before 15/08/2018
admin@svrinfotech.net
aniket@svrinfotech.net

Schedule of Session

DAY	Timing	AGENDA
Day 1	10.30 to 11.00	Inauguration and Introduction of workshop
	11.00 to 11.30	FEAST Development by VSSC ISRO & Its Importance.
	11.30 to 13.00	Guest Lecture on basic of FEA & its Importance, by VSSC/DAE Scientist
	13.00 - 14.00 Lunch Break	
	14.00 to 15.00	Introduction on FEAST SMT & GUI Representation
	15.00 to 16.00	Solving Numerical for Static Analysis, Modal
	16.00- 16.30 Tea Break	
	16.30 to 17.30	Solving Numerical for Static Buckling & Thermal
Day 2	10.30 to 11.00	Introduction of 1 D & 2 D Analysis in FEAST
	11.00 to 13.00	Hands on Practice to solve the Problems
	13.00 - 14.00 Lunch Break	
	14.00 to 14.30	Frequency Response, Heat Transfer (Steady State & Transient)
	14.30 to 16.30	Nonlinear, Coupled Field Analysis, Data Translation, Script Editing
	16.00- 16.30 Tea Break	
	16.30 to 17.30	Certificates & Vote of Thanks

Patron
Prof. O.P. Verma
Principal, GBPEC, New Delhi

Convener
Dr. Ajay K.S. Singholi (HOD, MAE)
ajay.singholi@gov.in

Presenter
Mr . Raj Thilak – VSSC, ISRO
Mr. Aniket Bhelsaikar – SVR InfoTech.

G.B. Pant Government Engineering College, New Delhi

Invites you for

Two Days Workshop

On

VSSC / ISRO's Developed FEAST^{SMT} Finite Element Analysis Technology

20th & 21st Aug 2018

Industry Partner

SVR InfoTech
Simplifying Virtual Reality

Organized by
MAE Department
Aug-2018

About the College

G.B Pant Engineering College was established by the Govt. of NCT of Delhi in the year 2007 with the aim of imparting quality technical education for the students of Delhi and nearby areas. Affiliated to Guru Gobind Singh Indraprastha University and approved by AICTE, the college started with two batches of students, one each for Mechanical & Automation Engineering and Electronics and Communications Engineering courses in the year 2007. Increasing its horizon in the year 2010, a batch of Computer Science & Engineering course was started in the college. With the presence of vibrant and dynamic faculty members of highest academic calibre selected by UPSC, G.B Pant has been able to produce three batches of engineering graduates who have been pursuing their careers in companies and higher education institutes in India and abroad.

Department of Mechanical & Automation Engineering

The department of MAE was established in 2007 with a vision to foster quality technical education in meeting the growing demand for technical man power in the industrial sector. The department offers (B.Tech.) program in MAE. The presence of experienced and highly talented faculty has helped to establish a high reputation for the department in the academia and industry.

About the FEAST^{SMT}

FEAST^{SMT} is ISRO's structural analysis software based on Finite Element Method (FEM) realized by Structural Engineering Entity of *Vikram Sarabhai Space Centre (VSSC).* The software is developed in C++ Programming Language based on Object Oriented (OO) principles.

The technology comprises of PreWin as Pre and Post Processor, with its own solver FEAST^{SMT}. With advanced solution algorithms the solver is able to handle large order problems of structural engineering.

This indigenous software is developed by VSSC/ISRO/DOS, Government of INDIA for all Computer Aided Engineering fraternity. Major of Colleges &corporate has adopted this technology for their FEA need.

FEAST^{SMT} Features

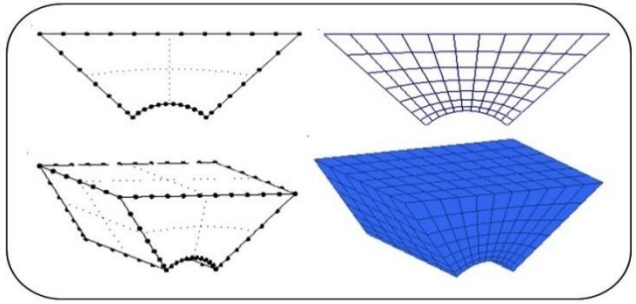
- Linear& Non Linear,
- Visco-elasticity,
- Transient response,
- Fluid-structure Interaction,
- Heat Transfer,
- Base excitation,
- Frequency response,
- Free Vibration, Buckling, etc….

Mesh Generation

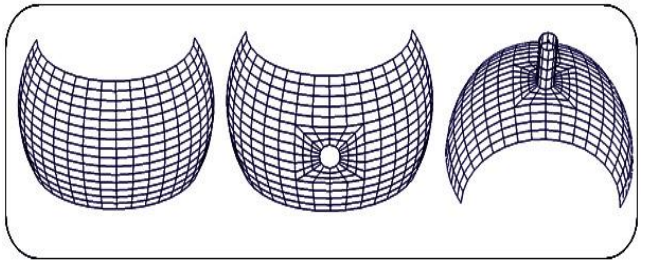
Supports mapped and automatic meshing techniques

- Bar2, Bar3
- Tria3, Tria6
- Tetra
- Quad4, Quad8
- Hexa8, Hexa20
- All QUAD auto mesh

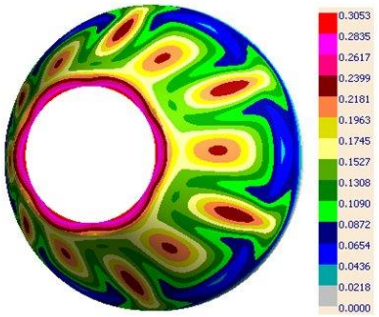
Initial FE mesh is generated on the geometry model and



further manipulated through the transformations and commands.



Post Processing



Multiple ways of visualizing the analysis results

- Deformed Geometry
- Animation
- Vector plot
- Contours
- X-Y plot
- Tabulated display